UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 6,983,360 B2
APPLICATION NO. : 09/943586
DATED : January 3, 2006
INVENTOR(S) : Thomas J. Kenney

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Replace informal drawing sheet 1 with the attached formal drawing sheet 1.

Replace informal drawing sheet 2 with the attached formal drawing sheet 2.

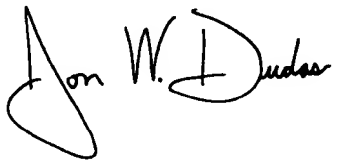
Replace informal drawing sheet 3 with the attached formal drawing sheet 3.

FIG. 4, decision box 418, change "none" to -- no data --.

Replace informal drawing sheet 4 with the attached formal drawing sheet 4.

Signed and Sealed this

Eighteenth Day of September, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office

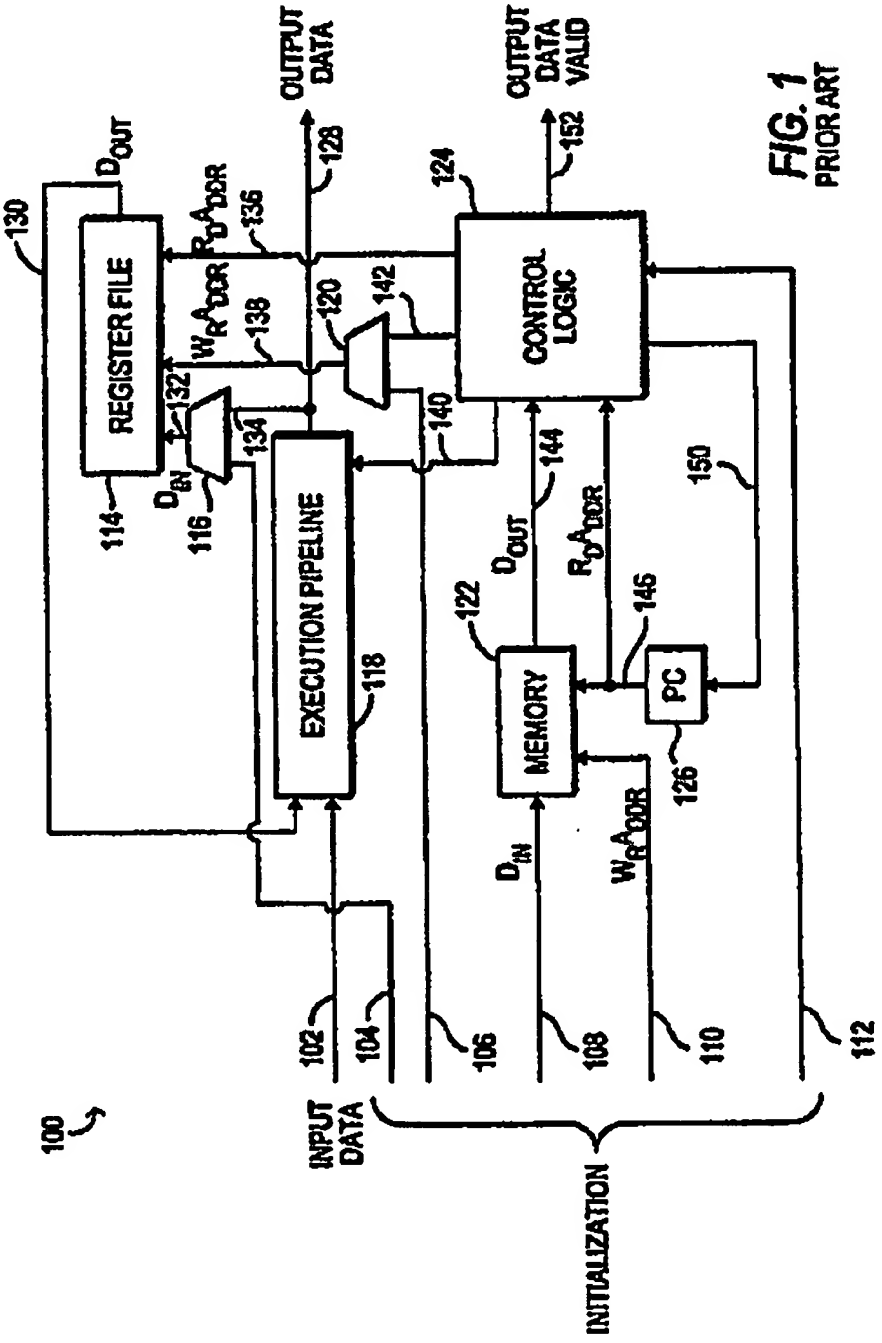


FIG. 1
PRIOR ART

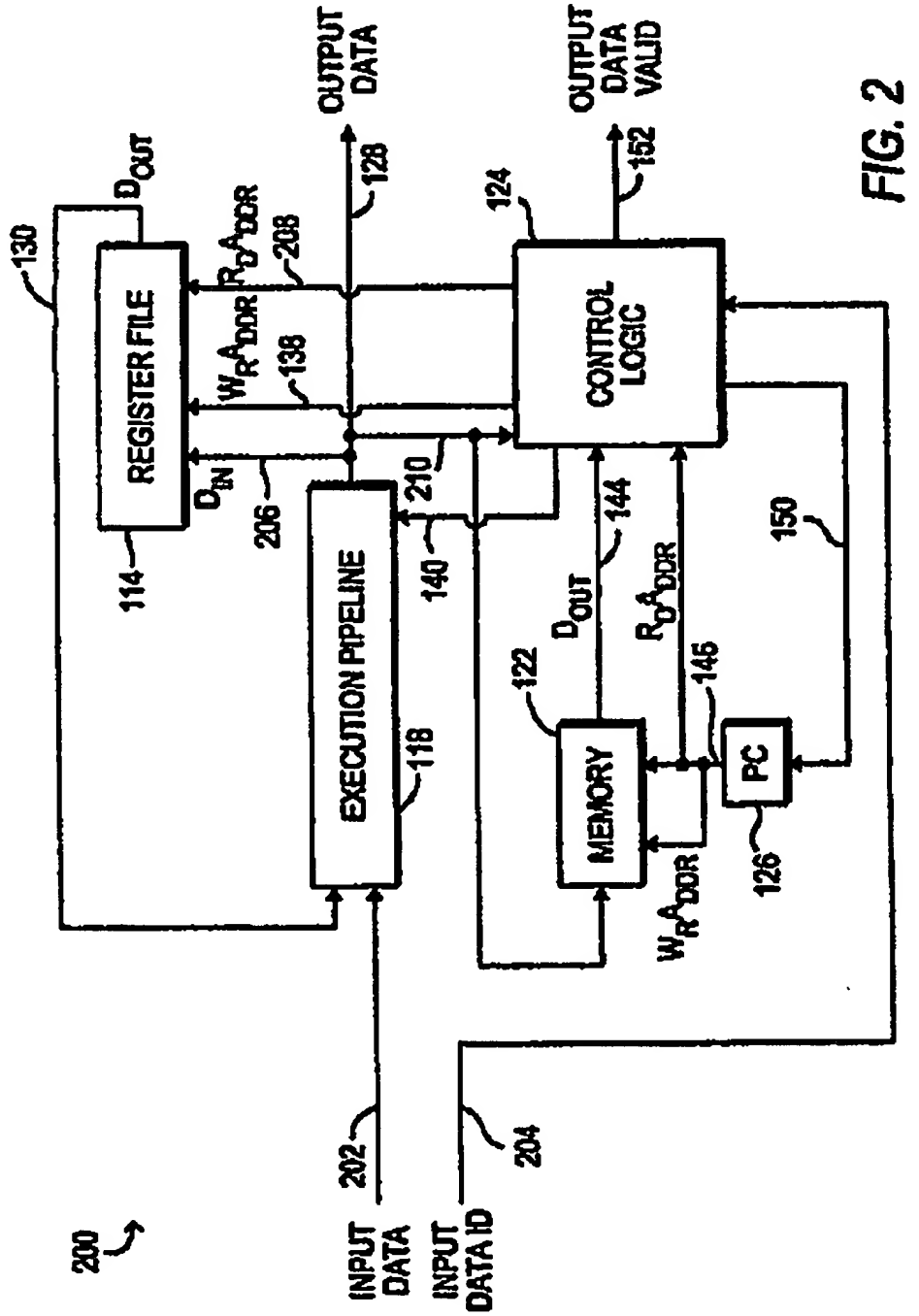

OUTPUT DATA
OUTPUT DATA VALID
128
152
124
130
D_{OUT}
R_DA_{DDR}
208
REGISTER FILE
CONTROL LOGIC
W_RA_{DDR}
138
D_{IN}
206
210
114
EXECUTION PIPELINE
140
144
150
D_{OUT}
R_DA_{DDR}
122
145
MEMORY
PC
126
W_RA_{DDR}
118
202
204
200
INPUT DATA
INPUT DATA ID

FIG. 2

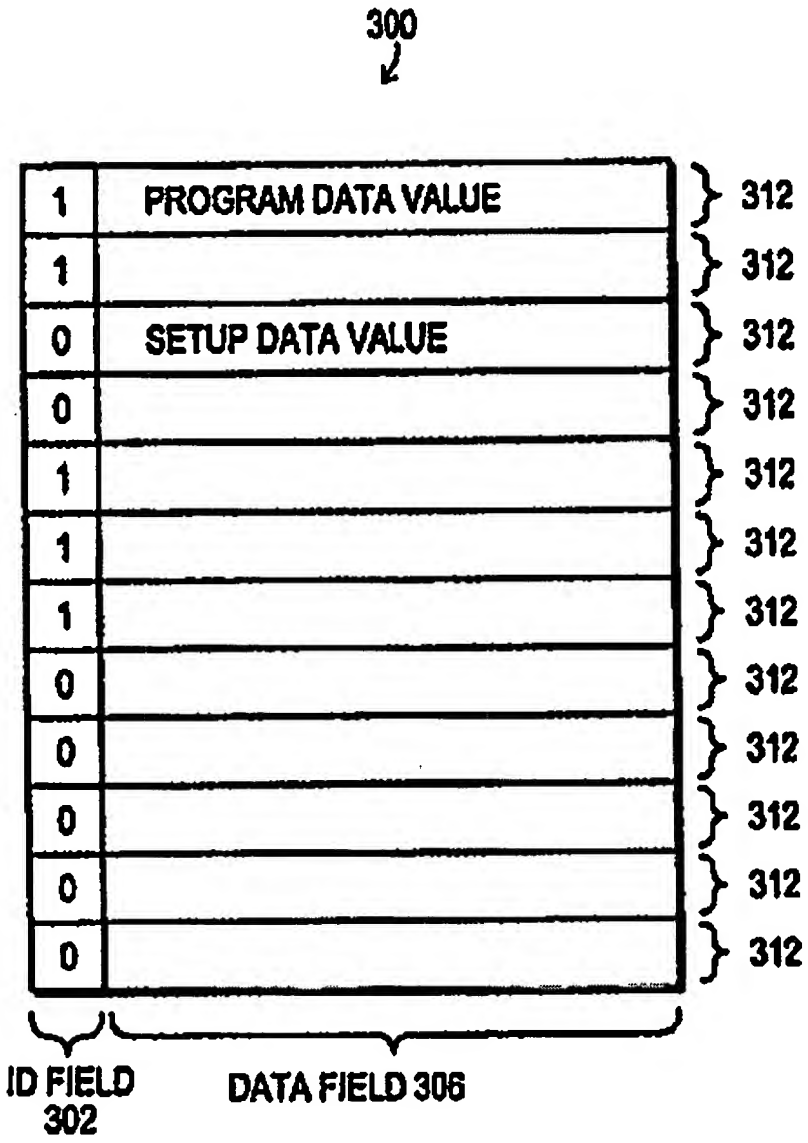

300
1
PROGRAM DATA VALUE
1
0
SETUP DATA VALUE
0
1
1
1
0
0
0
0
312
ID FIELD 302
DATA FIELD 306

FIG. 3

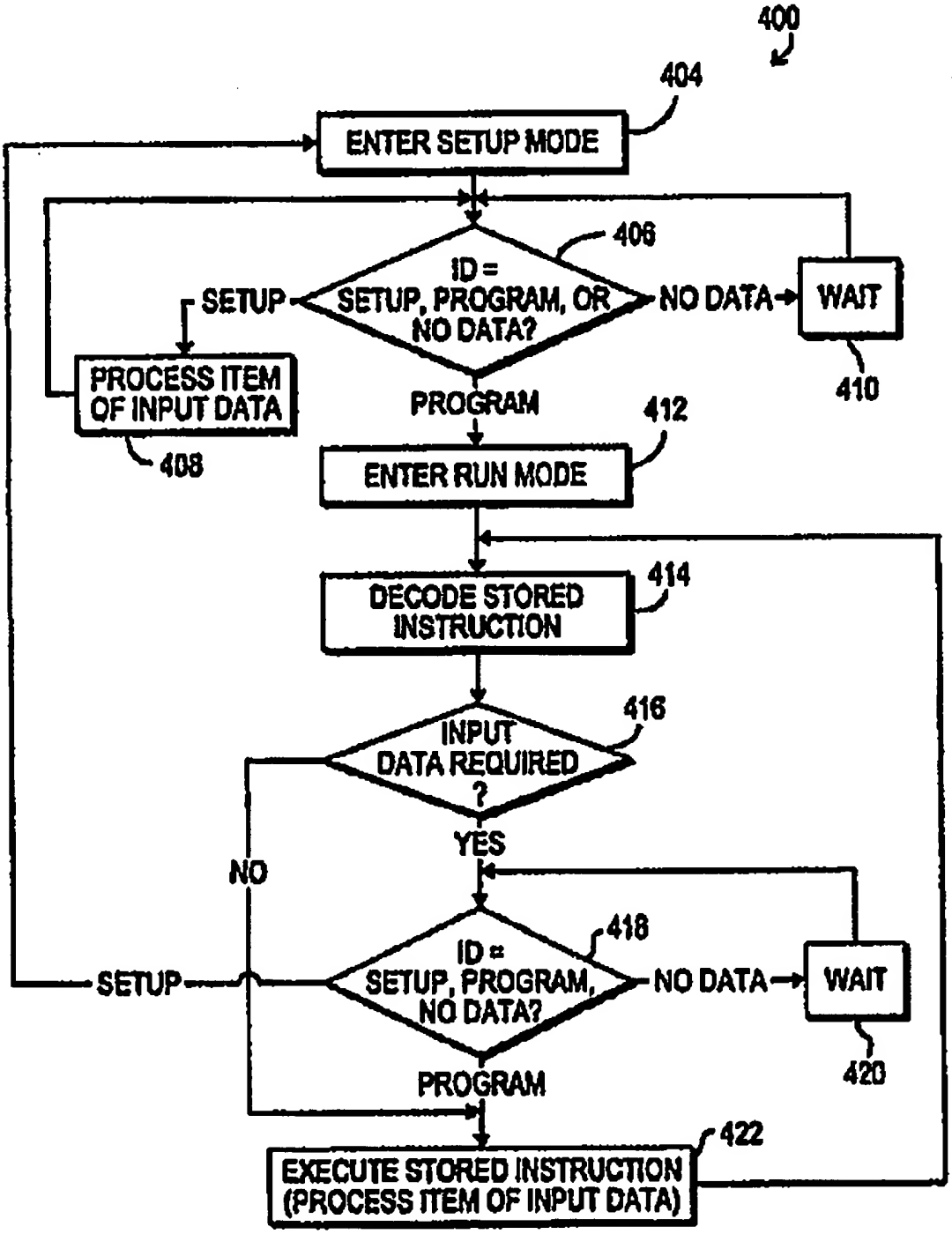

400
404
ENTER SETUP MODE
406
ID = SETUP, PROGRAM, OR NO DATA?
SETUP
NO DATA
WAIT
410
PROCESS ITEM OF INPUT DATA
408
PROGRAM
412
ENTER RUN MODE
414
DECODE STORED INSTRUCTION
416
INPUT DATA REQUIRED ?
YES
NO
418
ID = SETUP, PROGRAM, NO DATA?
SETUP
NO DATA
WAIT
420
PROGRAM
422
EXECUTE STORED INSTRUCTION (PROCESS ITEM OF INPUT DATA)

FIG. 4